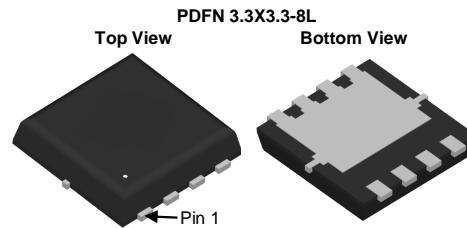


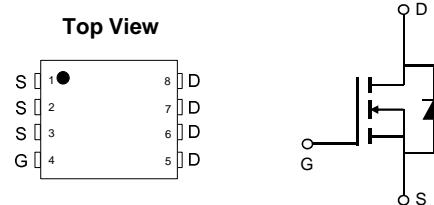
Feature

- 500V,3A
- $R_{DS\ (ON)} < 3.5\ \Omega @ V_{GS}=10V$
- Fast Switching
- Lead free product is acquired
- Excellent $R_{DS\ (ON)}$ and Low Gate Charge



Application

- PWM applications
- Load Switch
- Power management



ABSOLUTE MAXIMUM RATINGS ($T_a=25^\circ C$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	500	V
Gate-Source Voltage	V_{GS}	± 30	V
Continuous Drain Current ($T_a = 25^\circ C$)	I_D	3	A
Continuous Drain Current ($T_a = 100^\circ C$)	I_D	1.5	A
Pulsed Drain Current ⁽¹⁾	I_{DM}	8	A
Single Pulsed Avalanche Energy ⁽²⁾	E_{AS}	35	mJ
Power Dissipation	P_D	29	W
Thermal Resistance from Junction to Case	$R_{\theta JC}$	3.5	$^\circ C/W$
Junction Temperature	T_J	150	$^\circ C$
Storage Temperature	T_{STG}	-55 ~ +150	$^\circ C$



RHMN3N50

500V N-Channel MOSFET

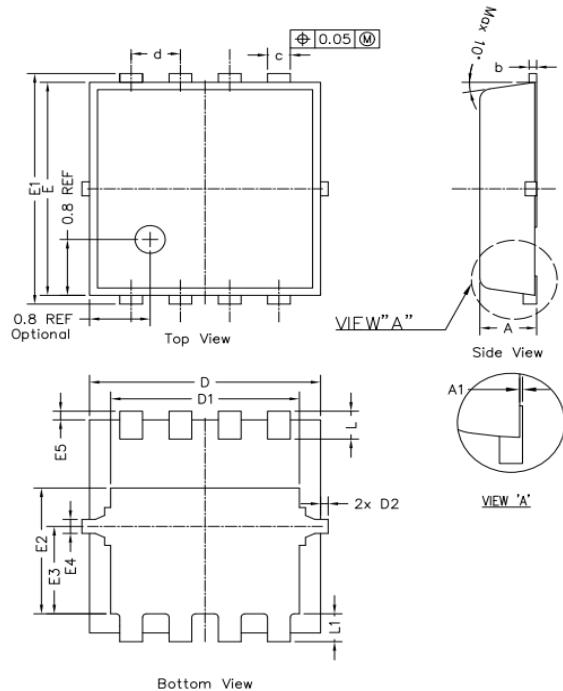
MOSFET ELECTRICAL CHARACTERISTICS($T_a=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Test Condition	Min	Type	Max	Unit
Static Characteristics						
Drain-source breakdown voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	500	-	-	V
Zero gate voltage drain current	I_{DSS}	$V_{\text{DS}} = 500\text{V}, V_{\text{GS}} = 0\text{V}$	-	-	1	μA
Gate-body leakage current	I_{GSS}	$V_{\text{GS}} = \pm 30\text{V}, V_{\text{DS}} = 0\text{V}$	-	-	± 100	nA
Gate threshold voltage ⁽³⁾	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	2	3.6	4	V
Drain-source on-resistance ⁽³⁾	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = 10\text{V}, I_D = 1.5\text{A}$	-	2.6	3.5	Ω
Forward transconductance ⁽³⁾	g_{FS}	$V_{\text{DS}} = 10\text{V}, I_D = 1.5\text{A}$	0.5	-	-	S
Dynamic characteristics						
Input Capacitance	C_{iss}	$V_{\text{DS}} = 25\text{V}, V_{\text{GS}} = 0\text{V}, f = 1\text{MHz}$	-	331	-	pF
Output Capacitance	C_{oss}		-	24	-	
Reverse Transfer Capacitance	C_{rss}		-	3	-	
Switching characteristics						
Turn-off delay time	$t_{\text{d}(\text{off})}$	$V_{\text{DD}} = 300\text{V}, I_D = 3\text{A}, V_{\text{GS}} = 10\text{V}, R_G = 25\Omega$	-	13	-	ns
Total Gate Charge	Q_g	$V_{\text{DS}} = 480\text{V}, I_D = 1\text{A}, V_{\text{GS}} = 10\text{V}$	-	4.8	-	nC
Gate-Source Charge	Q_{gs}		-	0.7	-	
Gate-Drain Charge	Q_{gd}		-	2.7	-	
Source-Drain Diode characteristics						
Diode Forward voltage ⁽³⁾	V_{DS}	$V_{\text{GS}} = 0\text{V}, I_S = 3\text{A}$	-	-	1.4	V
Diode Forward current ⁽⁴⁾	I_S		-	-	3	A
Body Diode Reverse Recovery Time	t_{rr}	$T_J = 25^\circ\text{C}, I_F = 3\text{A}, dI/dt = 100\text{A}/\mu\text{s}$		190		ns
Body Diode Reverse Recovery Charge	Q_{rr}	$T_J = 25^\circ\text{C}, I_F = 3\text{A}, dI/dt = 100\text{A}/\mu\text{s}$		0.53		uc

Notes:

1. Repetitive Rating: pulse width limited by maximum junction temperature
2. EAS Condition: $T_J = 25^\circ\text{C}, V_{\text{DD}} = 50\text{V}, R_G = 2.0\Omega, L = 10\text{mH}$
3. Pulse Test: pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
4. Surface Mounted on FR4 Board, $t \leq 10$ sec

Package Mechanical Data(PDFN3x3-8L)



SYMBOLS	DIMENSION IN MM			DIMENSION IN INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.700	0.750	0.800	0.028	0.030	0.031
A1	---	---	0.050	----	----	0.002
b	0.144	0.152	0.202	0.006	0.006	0.008
c	0.250	0.300	0.350	0.010	0.012	0.014
d	0.65 BSC			0.026 BSC		
D	2.950	3.050	3.150	0.116	0.120	0.124
D1	2.390	2.490	2.590	0.094	0.098	0.102
D2	---	---	0.125	---	---	0.005
E	2.950	3.050	3.150	0.116	0.120	0.124
E1	3.200	3.300	3.400	0.126	0.130	0.134
E2	1.700	1.800	1.900	0.067	0.071	0.075
E3	1.150	1.250	1.350	0.045	0.049	0.053
E4	0.150	0.200	0.250	0.006	0.008	0.010
E5	0.075	0.125	0.175	0.003	0.005	0.007
L	0.300	0.400	0.500	0.01	0.02	0.02
L1	0.300	0.400	0.500	0.01	0.02	0.02